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Applicant: Cheolsoo Park) I hereby certify that this paper is
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Serial No.: 10/748,466) States Postal Service with
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For: "Method of Manufacturing a) Box 1450, Alexandria, VA 22313-
Semiconductor Device") 1450 on this date:
Group Art Unit: Unknown) February 26, 2004
Examiner: Unknown	Mad H. Harry
	Mark G. Hanley
	Registration No. 44,736
) Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

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Corres	sponden	ce Address:
		Respectfully submitted,
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February 26, 2004

Form PTO-1449 (Modified) MAR 0 1 2004 INFORMATION STRONG SURE STATEMENT

(Use several sheets if necessary)

U.S. Department of Commerce Patent and Trademark Office Atty. Docket No. 20059/PIA31227 Serial No. 10/748,466

Applicant

Cheolsoo Park

Filing Date

Group Art Unit

December 30, 2003

Unknown

EXAMINER		DOCUMENT	ISSUE	NAME	CLASS	SUBCLASS	FILING DATE
INITIALS		NUMBER	DATE				IF APPROPRIAT
	A01	6,346,467	2/12/2002	Chang et al.			
	A02						
	A03						
	A04						
	A05						
	A06						
	A07						
	A08						
	A09						
	A10						-
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	A16						
	A17						
	A18						
	A19						
	A20						
	A21						
	A22						

EXAMINER

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Form PTO-1449 (Modified) RE STATEMENT INFORMATION DESCRIPTION

U.S. Department of Commerce Patent and Trademark Office

Atty. Docket No. 20059/PIA31227 Serial No. 10/748,466

Applicant

Cheolsoo Park

Filing Date

Group Art Unit Unknown

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December 30, 2003

		F	OREIGN PA	TENT DOCUN	IENTS			
*Examiner		Document	ocument Publication		Class	Subclass	Translation	
Initials		Number	Date				Yes	No
	B01							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)				
C01	Kim et al., Patent Application Publication No. 2002/0001935, January 2, 2002			
C02				

EXAMINER

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